

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	TECHNIQUES FOR CONFIGURING PROGRAMMABLE LOGIC USING ON-CHIP NONVOLATILE MEMORY							
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant: Thomas H. White</p> <p>Confirmation Number:</p> <p>Attorney Docket Number: 015114-070200US</p>								
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<table border="1"><thead><tr><th data-bbox="84 646 493 672">Submitted By:</th><th data-bbox="493 646 708 672">Elec. Sign.</th><th data-bbox="708 646 901 672">Sign. Capacity</th></tr></thead><tbody><tr><td data-bbox="84 672 493 719">Melvin D. Chan Registered Number: 39626</td><td data-bbox="493 672 708 719">Melvin D. Chan</td><td data-bbox="708 672 901 719">Attorney</td></tr></tbody></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Melvin D. Chan Registered Number: 39626	Melvin D. Chan	Attorney
Submitted By:	Elec. Sign.	Sign. Capacity						
Melvin D. Chan Registered Number: 39626	Melvin D. Chan	Attorney						

Documents being submitted:	Files
us-assignment	altera_702-usasn.xml us-assignment.xsl us-assignment.dtd 702_assn.tif
us-request	altera_702-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	altera_702-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	a1338_702-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd 702_fig1_3.tif 702_fig_2.tif 702_fig_4.tif 702_fig5.tif 702_fig6.tif 702_fig7.tif
us-power-of-attorney-grant	702_373b.tif
us-declaration	702_decl.tif
us-power-of-attorney-grant	702_poa.tif
Comments	